**Differences between memory mapped I/O and isolated I/O –**

| **ISOLATED I/O** | **MEMORY MAPPED I/O** |  |
| --- | --- | --- |
| Memory and I/O have separate address space | Both have same address space |  | |
| All address can be used by the memory | Due to addition of I/O addressable memory become less for memory |  | |
| Separate instruction control read and write operation in I/O and Memory | Same instructions can control both I/O and Memory |  | |
| In this I/O address are called ports. | Normal memory address are for both |  | |
| More efficient due to separate buses | Lesser efficient |  | |
| Larger in size due to more buses | Smaller in size |  | |
| It is complex due to separate separate logic is used to control both. | Simpler logic is used as I/O is also treated as memory only. |  | |